## AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Serial Number: 10/813,602 Filing Date: March 30, 2004

Title: NOVEL STRUCTURE AND METHOD FOR INTERRUPT DETECTION AND PROCESSING

Assignee: Intel Corporation

## IN THE SPECIFICATION

Please amend the paragraph beginning at line 8 on page 5, as follows:

Processor 110 polls system memory 140 to check for any interrupt information stored in system memory 140. If interrupt information is present, processor [[100]] 110 performs an interrupt function based on the interrupt information. Thus, in system 100, processor [[100]] 110 does not directly handle the interrupt information. Processor [[100]] 110 performs an interrupt function based on the interrupt information.